

TRAINING

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingssprache ist deutsch, falls nicht anders angekündigt.

Unter <http://www.FlowCAD.de/TrainingKontakt.php> können Sie sich zum Training anmelden.

Course Title	SIG3- Sigrity PowerDC and OptimizePI
Course Category	System Interconnect Design – Allegro & OrCAD
Duration	1 Day

"Very good, I think we could expect a lot of design process improvements with this tool. Nothing to improve further, it is great. The trainer is impressive in using the full toolchain. (Antoine Masson, Schneider Electric, June 2014)

"I like the fact that we took time to repeat the labs and that we could have a look on other types of simulation." (Flavien Dorlin, Schneider Electric, June 2014)

"I have really appreciated this course. The trainer was really motivated and well educated." (Thomas Weiss, Platinum, April 2015)

Course Description

Sigrity Power Integrity Suite provides a coherent methodology for the analysis of power delivery networks in high-speed printed circuit boards (PCBs). Power-delivery network design includes voltage regulator modules, decoupling capacitors, and power/ground planes. In this course, you use the Sigrity Power Integrity Suite software, to analyze a stable power distribution system to support high-speed circuit operation

Learning Objectives

After completing this course, you will be able to:

- Set up a board database for analysis
- Perform static IR-drop analysis
- Graph the impedance of the power distribution network across a complete frequency bandwidth
- Assign capacitor models to an existing printed circuit board
- Create and edit capacitor models
- Optimize capacitor selection for performance and cost

Software Used in This Course

- Allegro Power Integrity Suite

Course Agenda

- Introduction to Sigrity Power Integrity Suite
- Database Translation
- Static IR-drop Analysis
- Power Plane Impedance Analysis
- Capacitors and Plane Analysis Simulation

- Capacitor Optimization

Audience

- Analog Designers
- Design Engineers
- Electrical Engineers

Prerequisites

You must have

- Printed Circuit Board Design

Related Course:

- [SIG1AllegroSigritySIFoundations.pdf](#)