

XJTAG Boundary Scan Test



Product Comparison

Software

The XJTAG Development System contains the full suite of boundary scan applications – XJDeveloper, XJInvestigator, XJRunner and XJAnalyser. The XJTAG Professional Development System has all these software applications plus an XJIO board. This allows you to improve coverage by testing all the way through the connectors on your board.

	XJDeveloper	XJInvestigator	XJAnalyser	XJRunner XJRunner	
	XIDeveloper	XInvestigator	XJAnalyser		
Software Features	Integrated Develop- ment Environment for creating JTAG test and programming systems	Integrated test and debug environment for board repair/rework	Real-time visual analysis and debugging tool for devices in your JTAG chain	Run-time production test environment for executing XJDeveloper systems	
Test & Programming Development	\checkmark				
Test & Programming Execution	\checkmark	\checkmark		\checkmark	
Graphical Pin Display & Control		√	\checkmark		
Model Library for non-JTAG Devices	\checkmark				
Interconnect/Connection Test	create + run	run		run	
Detailed Fault Description	\checkmark	√		\checkmark	
Flash Programming	create + run	run		run	
FPGA/CPLD Programming	create + run	run	create + run	run	
Parallel Testing				\checkmark	
Layout Viewer	\checkmark	\checkmark		\checkmark	
Schematic Viewer	\checkmark	√		\checkmark	
Waveform Viewer		√	\checkmark		
Test Coverage Analysis & Reports	\checkmark				
XJEase-Debugger	\checkmark				
XJFlash Compatible	create + run	run		run	
XJDirect Compatible	create + run	run		run	
External DLL calls	create + run	run		run	
Netlist Merging	\checkmark	\checkmark		\checkmark	
BSDL Editor	\checkmark				
Testing Without Netlist	create + run	run		run	
Logic Device Support	create + run	run		run	
Fault Dictionary	create + run	create + run		create + run	
3rd Party Integration				\checkmark	
Golden Scan / Golden Board Comparison		\checkmark	\checkmark		
Demo-Board and Tutorials	\checkmark	\checkmark	\checkmark	\checkmark	
JTAG Chain Debugger	\checkmark	\checkmark	\checkmark	\checkmark	
Support and Maintenance	\checkmark	\checkmark	\checkmark	\checkmark	

Hardware

XJTAG is easy to use on multiple machines. Licenses are not locked to a PC; they can be stored in the hardware interface or taken from a network license server, while any of these portable hardware options allow you to connect your computer with your circuit.

	Expert ADF-2	XJLink2	PXI-XJLink2	XJLink2-3070	XJLink2-CFM	XJQuad	XJLink
Hardware Features							
Configurable Voltage	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Frequency Measurements	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Voltage Banks	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Voltage Measurements	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Number of JTAG Controllers	1	1	1	1	1	4	1
TAPs per JTAG Controller	4	4	4	4	4	4	1
GPIO on Pins	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Adjustable JTAG Signal Termination	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Automatic Signal Skew Control	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark
Adjustable Signal Slew	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	
Button to Start Tests		\checkmark				\checkmark	
Visual Indication of Test Status		\checkmark	\checkmark			\checkmark	
TCK Frequencies (up to)	166 MHz	166 MHz	166 MHz	166 MHz	166 MHz	166 MHz	50 MHz
Digital Oscilloscope	\checkmark						
Spectrum Analyser	\checkmark						
Waveform & Function Generator	\checkmark						
Serial Protocols Analyser	\checkmark						
Connection	USB	USB	PXI	Keysight (Agilent) i3070	Teradyne TestStation	USB	USB



Try XJTAG Boundary Scan with your own board

- Advanced tools for testing, debugging & programming complex electronic boards and systems
- Works with most PCBs: uses the JTAG protocol built into FPGAs, CPLDs and most CPUs
- All XJTAG software with full features + JTAG controller hardware + Interactive tutorials + Tech support

30 Days Free Trial



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