

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingsprache ist deutsch, falls nicht anders angekündigt.

Allegro PCB Router Basis

Overview

Description

This is an Engineer Explorer class that is designed around more advanced topics and exploration of the tool. This course does not teach basic tool operations. We require that students who are not actively using the tool first complete the [Allegro PCB Router course](#).

The Allegro PCB Router Advanced Techniques course uses a series of lectures, examples, and hands-on experience to deliver the information you need to further your knowledge and increase the performance of the autorouting tools.

Learning Objectives:

- Expand your knowledge of the PCB Router
- Explore key areas of the core Router database
- Explore the under-utilized features of do file programming
- Learn about options and syntax
- Write your own do file program
- Learn how the PCB Router handles more difficult routing challenges, beginning with differential pairs
- Review constraints and route diff pairs
- Study blind and buried vias and all their options
- Learn rules for matched length nets
- Examine and exercise different routing challenges

Audience

- This course is for PCB Designers and others who wish to learn more about the PCB Router tool

Software

- Allegro PCB Design Expert 610
- Allegro PCB Router 610

Prerequisites

- You should have completed the [Allegro PCB Router](#) class within the last two years
- You are expected to have used SPECCTRA tool for six months AND routed at least six production boards
- You should have experience creating and using do files that define rules and execute routing commands. You should also have a full understanding of the rule precedence hierarchy in SPECCTRA
- You should have a full understanding of the host CAD translator you are using. NO time is spent on translation issues in this class.

Related Courses

- [Allegro PCB Router](#)

Course Agenda

- Explore and decipher the Router Design File database
- Learn about do file programs and then write a program
- Explore and route Differential Pair nets
- Take an in-depth look at blind and buried vias
- Determine and use the best methods of routing matched length nets
- Apply all the techniques you have learned in a final, all inclusive lab.