

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingsprache ist deutsch, falls nicht anders angekündigt.

Allegro Package Designer

Description

This course introduces the Cadence® Advanced Package Designer system. It covers the design and specifications for manufacturing single-chip, few-chip, and multi-chip modules for single-, double-, or multi-layered analog and digital packages.

The highlights of this class include the following:

- Process flow
- Library definition
- Database setup
- Bond and blind/buried padstack editing
- Extraction of user-defined information
- Z router
- Construction of single-chip, few-chip, and multi-chip modules
- Interactive routing techniques
- Gerber, DXF, and stream output

Audience

- This course is for Package MCM designers and design engineers.

Software

- Cadence® Advanced Package Designer

Prerequisites

- You should have a working knowledge of single- and/or multi-chip design and construction.
- You should be familiar with foundry design guidelines
- You should have a basic understanding of the UNIX or WindowsNT operating systems.

Related Courses

- [Allegro® PCB Layout System](#)
- [Concept® HDL Front-to-Back Design Flow](#)

Course Agenda

- Getting Started
 - Terms and Definitions
 - Graphical User Interface
 - Database Elements

- Commands
 - directory Structure
 - Environment Variables
- Component Building
 - Layer Stackup
 - BGA
 - Die
 - Page Border
- Design Setup
 - Creating a netlist
 - Keepout/Keepin Areas
 - Default Constraints
 - Extended Constraints
- Power Rings and Wire Bonding
 - Creating Power Rings
 - Wire Bonding
- Net Editing
 - Creating Nets
 - Assigning Nets
 - De-assigning Nets
- Routing
 - Pin Escaping
 - Interactive Routing
 - Auto Routing
 - Adding Power Planes
- Design Verification and Manufacturing Output
 - Reports - Summary
 - Unplaced Components
 - DRC
 - Netlist
 - Trace Cleanup - Dangling Lines
 - Custom Smooth
 - Fillets

- Bond Wire Diagrams
- Photoplot - RS274X
- Exporting DXF
- GDSII Stream
- Dimensioning
- Plotting