FlowCAD

TRAINING

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingssprache ist deutsch, falls nicht anders angekündigt.

Course Title	**System SI Parallel Bus Analysis**
Course Category	**System Interconnect Design – Allegro & OrCAD**
Duration	**1 Day**

Course Description

This course covers the analysis of a DDR2 parallel bus interface to verify the operation of the bus using System SI Parallel Bus Analysis. You will define a DDR2 interface for a system and assign IBIS models to the components of the system. You will also learn how to add the power rail information to the IBIS models to include power distribution effects in the analysis. An S Parameter model will be extracted from a PCB database to be used in the simulation. You will generate a Bus Analysis report to verify that successful operation of the bus interface.

Learning Objectives

After completing this course, you will be able to:

- Define a system in System SI
- Assign IBIS models to the components in the system
- Augment IBIS models to include power distribution effects
- Use an S-Parameter model for the PCB component of the system
- Set analysis options
- Run a parallel bus analysis
- View waveforms from a parallel bus analysis
- Generate a Bus Analysis Report

Software Used in This Course

- System SI – Parallel Bus Analysis Configuration

Software Release(s)

- Sigrity 12.0

Course Agenda

Note that this course can be tailored to better meet your needs – contact the Cadence training staff *for specifics.*

Day 1

- Analyzing a parallel bus
- IBIS file augmentation
- Design entry
- Assigning device models
- Interconnect models
- Adding a VRM
- Connecting the system
- Simulation
- DDR measurement report

Audience

- Electrical Engineers
- PCB Designers

Prerequisites

You must have

- A familiarity with digital and analog circuit design methodology
- A working knowledge of PCB signal analysis and transmission line theory

Related Courses

- **Allegro PCB SI Foundations v16.5**