

# TRAINING

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingssprache ist deutsch, falls nicht anders angekündigt.

<b>Course Title</b>	<b>Allegro FPGA System Planner v16.3</b>
<b>Course Category</b>	<b>System Interconnect Design – Allegro &amp; OrCAD</b>
<b>Duration</b>	<b>2 Days</b>
<b>Course ID</b>	<b>ES_84479_16.3</b>
<b>Product Version</b>	<b>16.3</b>

## Course Description

In the Allegro® FPGA System Planner (FSP) course, you learn to define an FPGA system and synthesize the connections in the design. You generate a schematic and PCB Editor database so the FPGA I/O assignments can be optimized in the board environment.

## Learning Objectives

In this course, you

- Identify how data flows from the FPGA System Planner (FSP) to the schematic and PCB
- Create a design in FSP
- Define the protocols and interfaces in an FSP design
- Synthesize the connections in FSP protocols and interfaces
- Add terminations and external ports in an FSP design
- Generate an Allegro Design Entry HDL schematic from your FSP design
- Export your FSP placement into the PCB Editor
- Backannotate pin swaps and design changes from the schematic and PCB Editor to FSP

## Software

Allegro FPGA System Planner XL  
Allegro PCB Design HDL XL

## Course Agenda

### Day 1

- Identify how data flows from the Allegro FPGA System Planner to the schematic and PCB.
- Create a design in FSP.
- Define the protocols and interfaces in an FSP design.
- Synthesize the connections in FSP protocols and interfaces.

### Day 2

- Add terminations and external ports in an FSP design.
- Generate an Allegro Design Entry HDL schematic from your FSP design.
- Export your FSP placement to the PCB Editor.
- Backannotate pin swaps and design changes from the schematic and PCB Editor to FSP.

## Audience

- Design Engineers
- FPGA Designers
- PCB Designers

## Prerequisites

You need to have experience with or already have knowledge of the following:

- Logic Design

You need to have completed the following courses:

- [Allegro Design Entry HDL Front-to-Back Flow 16.3](#)
- [Allegro PCB Editor 16.3](#)

You need experience with the following software:

- Allegro Design Entry HDL XL
- Allegro PCB Design HDL XL

## Related Courses

- [Allegro Design Entry HDL Front-to-Back Flow v16.3](#)
- [Allegro PCB Editor Basic Techniques v16.3](#)