

TRAINING

Bei dem hier beschriebenen Training handelt es sich um ein Cadence Standard Training. Sie erhalten eine Dokumentation in englischer Sprache. Die Trainingssprache ist deutsch, falls nicht anders angekündigt.

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| Course Title | Allegro AMS Simulator v16.3 |
| Course Category | System Interconnect Design – Allegro & OrCAD |
| Duration | 3 Days |
| Course ID | ES_86087_16.3 |
| Product Version | 16.3 |

Course Description

The Allegro® AMS Simulator course starts with the basics of entering a design for simulation and builds a solid foundation in the overall use of the software. You run DC Bias simulations, transient analysis simulations, and sweep simulations, allowing you to sweep component values, operating frequencies, or global parameters. You also have the opportunity to simulate several types of analog circuits, transformers, digital circuits, and mixed analog and digital circuits.

Learning Objectives

After completing this course you will be able to:

- Enter a design for simulation
- Run DC bias, DC sweep, and AC sweep analyses
- Edit a stimulus and run a parametric analysis
- Edit models, run a Monte Carlo analysis, create subcircuits, and create parts for simulation from a model or subcircuit definition
- Create linear and non-linear transformers, and perform temperature, worst-case, and noise analysis
- Apply analog behavioral modeling and run digital and mixed analog and digital simulation

Software Used in This Course

- Allegro AMS Simulator

Software Release(s)

- SPB1601

Course Agenda

Note that this course can be tailored to better meet your needs – [contact the Cadence training staff](#) for specifics.

Day 1

- Building a design for simulation
- Setting up and running DC bias point analysis
- Setting up and running DC and AC sweep analyses
- Viewing simulation results in the probe window
- Setting up sources and using markers
- Creating and simulating a text netlist
- Accessing the stimulus editor using VSTIM, ISTIM, and DIGSTIM Running transient analysis

Day 2

- Examining common simulation errors
- Creating linear and non-linear transformers
- Setting up and running parametric analysis
- Creating a subcircuit
- Performing temperature analysis
- Configuring and running Monte Carlo analysis
- Simulating with hierarchical blocks and symbols

Day 3

- Running simulations using analog behavioral modeling
- Using digital components in a design
- Combining analog and digital components in designs
- Using performance analysis and creating goal functions
- Setting up and running worst-case analysis
- Setting up and running noise analysis

Audience

- Analog Designers
- Electrical Engineers

Prerequisites

You must have completed the following course:

- [Allegro Design Entry HDL Front-to-Back Flow v16.3](#)

Related Courses

- [Allegro Design Entry HDL Front-to-Back Flow v16.3](#)